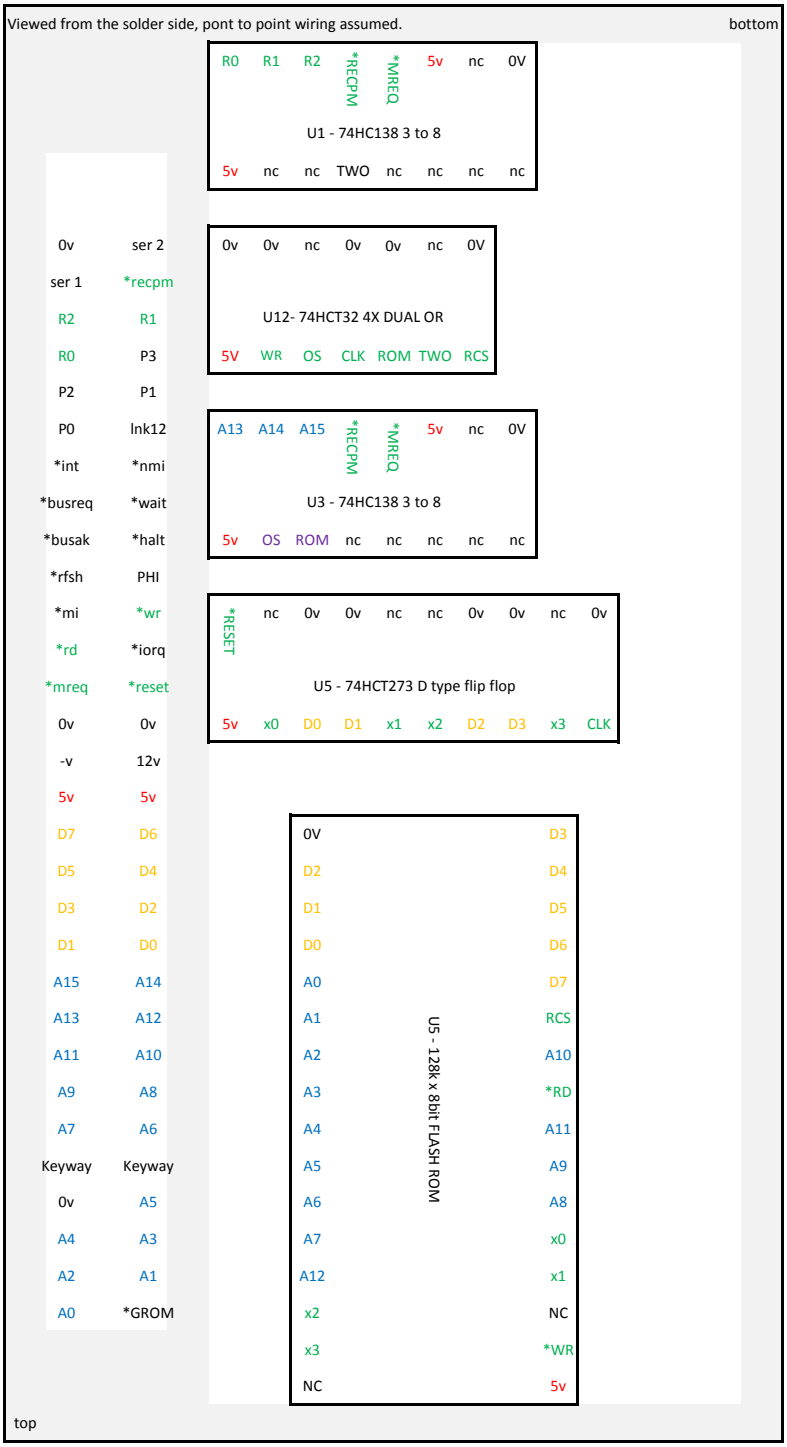
Pascal Rom board 0.1



Based off MAGROM 5.1

128k flash in ROM 2 so 16 pages of 8k
Write to OS rom sets the rom paging register
Based on the PAL on PG250 of the manual, there's no read check on rom access

Decoding for ROM2
A13 =1 A14=0 A15=0 R0=0 R1=1 R2=0 MREQ=0 RELCPMH=0
Feed MREQ and RELCPMH to a 138 low enables
R0=A R1=B R2=C to select lines
Y2 or the other Y1 = Rom select

Decoding for paging register
A13=0 A14=0 A15=0 MREQ=0 WR=0 RELCPMH=0
Feed MREQ and RELCPMH to a 138 low enables
A13 = A A14 = B A15= C select lines
register write then becomes Y0 OR WR

Paging register bits go to high address lines on the rom
4 ofr 128k (or 2 for 32k, 6 for 512k)